

CP5073 EMBEDDED SOFTWARE DEVELOPMENT

DETAILED SYLLABUS

OBJECTIVES:

- To understand the architecture of embedded processor, microcontroller and peripheral devices.
- To interface memory and peripherals with embedded systems.
- To study the embedded network environment.
- To understand challenges in Real time operating systems.
- To study, analyze and design applications on embedded systems.

UNIT I EMBEDDED PROCESSORS

Embedded Computers - Characteristics of Embedded Computing Applications - Challenges in Embedded Computing System Design - Embedded System Design Process- Formalism for System Design - Structural Description - Behavioural Description - ARM Processor - Intel ATOM Processor.

UNIT II EMBEDDED COMPUTING PLATFORM

CPU Bus Configuration - Memory Devices and Interfacing - Input/Output Devices and Interfacing - System Design - Development and Debugging – Emulator – Simulator - JTAG Design Example – Alarm Clock - Analysis and Optimization of Performance - Power and Program Size.

UNIT III EMBEDDED NETWORK ENVIRONMENT

Distributed Embedded Architecture - Hardware and Software Architectures - Networks for Embedded Systems - I2C - CAN Bus - SHARC Link Supports – Ethernet – Myrinet – Internet - Network-based Design - Communication Analysis - System Performance Analysis – Hardware Platform Design - Allocation and Scheduling - Design Example - Elevator Controller.

UNIT IV REAL-TIME CHARACTERISTICS

Clock Driven Approach - Weighted Round Robin Approach - Priority Driven Approach – Dynamic versus Static Systems - Effective Release Times and Deadlines - Optimality of the Earliest Deadline First (EDF) Algorithm - Challenges in Validating Timing Constraints in Priority Driven Systems - Off-Line versus On-Line Scheduling.

UNIT V SYSTEM DESIGN TECHNIQUES

Design Methodologies - Requirement Analysis – Specification - System Analysis and Architecture Design - Quality Assurance - Design Examples - Telephone PBX - Ink jet printer - Personal Digital Assistants - Set-Top Boxes.

REFERENCES:

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