

EC8011 DSP PROCESSOR ARCHITECTURE AND PROGRAMMING

DETAILED SYLLABUS

OBJECTIVES:

The objective of this course is to provide knowledge on:

- Basics on Digital Signal Processors
- Programmable DSP's Architecture, On-chip Peripherals and Instruction set
- Programming for signal processing applications
- Advanced Programmable DSP Processors

UNIT I FUNDAMENTALS OF PROGRAMMABLE DSPs

Introduction to Programmable DSPs, Architectural Features of PDSPs - Multiplier and Multiplier accumulator – Modified Bus Structures and Memory access – Multiple access memory – Multi-port memory – VLIW architecture- Pipelining – Special Addressing modes in P-DSPs – On chip Peripherals, Applications of Programmable DSPs.

UNIT II TMS320C5X PROCESSOR

Architecture of C5X Processor – Addressing modes – Assembly language Instructions - Pipeline structure, On-chip Peripherals – Block Diagram of DSP starter kit (DSK) – Software Tools, DSK on-board peripherals, Application Programs for processing real time signals.

UNIT III TMS320C6X PROCESSOR

Architecture of the C6x Processor - Instruction Set – Addressing modes, Assembler directives, On-chip peripherals, DSP Development System: DSP Starter Kit - Code Composer Studio - Support Files – Introduction to AIC23 codec and other on-board peripherals, Real-Time Programming Examples for Signals and Noise generation, Frequency analysis, Filter design.

UNIT IV ADSP PROCESSORS

Architecture of ADSP-21XX and ADSP-210XX series of DSP processors- Addressing modes and assembly language instructions – Application programs –Filter design, FFT calculation.

UNIT V ADVANCED PROCESSORS

Study of TI's advanced processors - TMS320C674x and TMS320C55x DSPs, ADSP's Blackfin and Sigma DSP Processors, NXP's DSP56Fxx Family of DSP Processors, Comparison of the features of TI, ADSP and NXP DSP family processors.

REFERENCES:

1. B. Venkataramani and M. Bhaskar, —Digital Signal Processors – Architecture, Programming and Applicationsll – Tata McGraw – Hill Publishing Company Limited. New Delhi, 2003.
2. Avtar Singh and S. Srinivasan, Digital Signal Processing – Implementations using DSP Microprocessors with Examples from TMS320C54xx, Cengage Learning India Private Limited, Delhi 2012.

Diploma, Anna Univ UG & PG Courses

Notes

Syllabus

Question Papers

Results and Many more...

Available @

www.AllAbtEngg.com

3. Rulph Chassaing and Donald Reay, Digital Signal Processing and Applications with the C6713 and C6416 DSK, John Wiley & Sons, Inc., Publication, 2012 (Reprint).

4. User guides Texas Instruments, Analog Devices and NXP.